

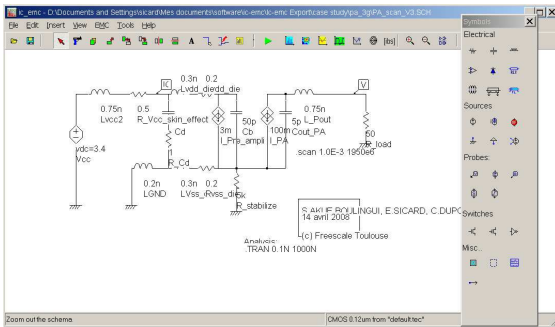
IC-EMC, a Software Demonstrator for predicting Electromagnetic Compatibility of Integrated Circuits

Alexandre Boyer, Etienne Sicard, INSA of Toulouse, France
alexandre.boyer@insa-toulouse.fr

Abstract

The proposed computer demonstration concerns the freeware IC-EMC, a windows-based software demonstrator which aims at simulating parasitic emission and susceptibility of integrated circuits. IC-EMC is not only a tool dedicated to research about EMC of integrated circuits, but also a support for teaching EMC.

- IBIS file editor and I/O model display
- XML scan data viewer
- Package viewer and package parasitic extraction
- PCB Parasitic interconnect extraction
- ICEM model generator



The tool IC-EMC includes a conventional schematic editor, an interface to WinSpice for analog simulation and a set of post-processors dedicated to simulation of:

- IC conducted and radiated emission
- Near field radiated by ICs
- IC conducted and radiated susceptibility
- Z and S parameters

References

- [1] The latest version of the IC-EMC software may be downloaded from www.ic-emc.org.
- [2] E. Sicard, A. Boyer "IC-EMC v2.0 User's Manual", INSA editor, July 2009, ISBN 978-2-87649-056-7, 310 pp

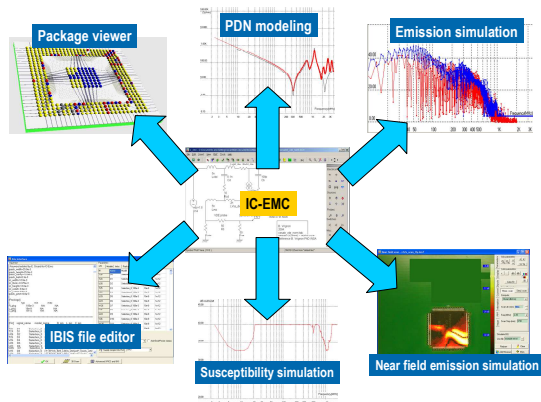
About the Authors



Alexandre Boyer is an Assistant Professor in the Department of Electrical and Computer Engineering at INSA of Toulouse. His research interests are IC susceptibility modelling, reliability of IC's and CAD tool development for EMC.



Etienne SICARD is a professor at INSA of Toulouse, France, Department of Electrical and Computer Engineering. His research interests include several aspects of CAD tools for the design and electromagnetic compatibility of integrated circuits.



IC-EMC includes also a library of common EMC elements, examples and tools dedicated to EMC model extraction as: